

forming at least two openings in the layer for a capacitor having a first electrode and a second electrode, wherein the first and second electrodes are each defined by an opening that extends through the stop layer and the layer.

a¹
level
[2] Please amend Claim 2 as follows:]

2. (Amended) The method of claim 1 wherein forming an opening for a dual damascene structure and forming at least two openings in the layer for a capacitor occur at substantially the same time.

(3) Please cancel Claims 3-5 without prejudice or disclaimer.

(4) Please amend Claim 6 as follows:

6. (Amended) The method of claim 1 further comprising:

filling the opening for a dual damascene structure with a conductive material; and

filling the at least two openings in the layer for a capacitor with a conductive material.

a²
[5] Please amend Claim 7 as follows:]

7. (Amended) The method of claim 6 wherein filling the opening for a dual damascene structure and filling the at least two openings in the layer for a capacitor occur at substantially the same time.

(6) Please cancel Claim 9 without prejudice or disclaimer.

(7) Please amend Claim 10 as follows:

10. (Amended) A method of manufacturing an integrated circuit comprising:

forming a plurality of layers;

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2 partially forming a dual damascene structure by forming a first opening in a least one of the plurality of layers; and

partially forming a capacitor by forming second and third openings in the at least one of the plurality of layers, wherein the second and third openings extend through the at least one of the plurality of layers.

REMARKS/ARGUMENTS

The Applicant has carefully considered this application in connection with the Examiner's Action and respectfully request reconsideration of this application in view of the foregoing amendment and the following remarks.

The Applicant originally submitted Claims 1-22 in the application. The Applicant previously elected to pursue Claims 1-12 and canceled Claims 13-22. Herein, the Applicant has amended Claims 1-2, 6-7 and 10 and has canceled Claims 3-5 and 9. Accordingly, Claims 1-2, 6-8 and 10-12 are currently pending in the application.